

UTILITY PATENT APPLICATION TRANSMITTAL UNDER 37 C.F.R. §1.53(b)

ASSISTANT COMMISSIONER FOR PATENTS

Box PATENT APPLICATION

Washington D.C. 20231

Case Docket No. IK-011

Sir:

Transmitted herewith for filing is the patent application of
INVENTOR OR APPLICATION IDENTIFIER: Sang-Seog KANG
FOR: GLASS TOUCH SENSING CIRCUIT

Enclosed are:

1. [X] 26 pages of specification, claims, abstract
2. [X] 3 sheets of FORMAL drawing.
3. [X] 2 pages of newly executed Declaration & Power of Attorney (original).
4. [X] Priority Claimed to Korean Appln. No. 2000-32413, whose entire disclosure is incorporated herein by reference.
5. [] Small Entity Statement.
6. [] Information Disclosure Statement, Form PTO-1449 and reference.
7. [X] Assignment Papers for LG Electronics Inc. (cover sheet, assignment & assignment fee).
8. [X] Certified copy of Korean Patent Application No. 2000-32413 filed June 13, 2000.
9. [X] Two (2) return postcards.
- [X] Stamp & Return with Courier.
- [X] Prepaid Postcard-Stamped Filing Date & Returned with Unofficial Serial Number.
10. [X] Authorization under 37 C.F.R. §1.136(a)(3).
11. [] Other:

CLAIMS AS FILED

For	No. Filed		No. Extra	Rate	Fee
Total Claims	8	- 20	0	X \$18.00	\$0.00
Indep. Claims	1	- 3	0	X \$80.00	\$0.00
Multiple Dependent Claims (If applicable)				X \$270.00	\$0.00
				BASIC FEE	\$710.00
				TOTAL FILING FEE	\$710.00

- [] This is a Continuation-in-part (CIP) of prior application No: _____ filed _____. Incorporation By Reference-The entire disclosure of the prior application is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
- [] Amend the specification by inserting before the first line the sentence:
-This application is a continuation-in-part of Application Serial No. _____ filed _____.-
- [X] A check in the amount of \$710.00 (Check #9689) is attached.
- [] Please charge my Deposit Account No. 16-0607 in the amount of \$_. A duplicate copy of this sheet is enclosed.
- [X] The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 16-0607. A duplicate copy is enclosed.
- [X] Any additional filing fees required under 37 C.F.R. 1.16.
- [X] The Commissioner is hereby authorized to charge payment of following fees during the pendency of this application or credit any overpayment to Deposit Account No. 16-0607. A duplicate copy of this sheet is enclosed.
- [X] Any patent application processing fees under 37 C.F.R. 1.17.
- [X] Any filing fees under 37 C.F.R. 1.16 for presentation of extra claims.

FLESHNER & KIM, LLP

Daniel Y.J. Kim
Registration No. 36,186

Correspondence Address Below:

P.O. Box 221200

Chantilly, VA 20153-1200

(703) 502-9440 DYK/lam

Date: November 3, 2000

GLASS TOUCH SENSING CIRCUIT

BACKGROUND OF THE INVENTION

5 Field of the Invention

The present invention relates in general to glass touch sensing circuits, and more particularly to a glass touch sensing circuit which is capable of accurately detecting a sense signal resulting from a user's touch under no influence
10 of variations in temperature.

Description of the Prior Art

Glass touch sensing circuits are generally adapted to input a command generated when the user touches a specific
15 area on a display screen. These sensing circuits are used for the input of commands in a variety of household electric appliances and electronic products such as high-class microwave ovens, notebook computers, display screens, televisions, etc.

20 Fig. 1 is a circuit diagram showing the construction of a conventional glass touch sensing circuit. As shown in this drawing, the conventional sensing circuit comprises a touch sensor 5 for outputting a sense signal in response to a user's touch, and a resistor R1 and capacitor C1 interacting to
25 convert the sense signal from the touch sensor 5 into a

switching signal to a transistor Q1, which will hereinafter be described in detail.

The resistor R1 has its one end connected to an output terminal of the touch sensor 5 and its other end connected to a connection point A. Commonly connected to the connection point A are one end of the capacitor C1, one end of a resistor R4 and a base terminal of the transistor Q1. The other end of the capacitor C1, the other end of the resistor R4 and an emitter terminal of the transistor Q1 are connected in common to a first output terminal OUT1 of a microprocessor 15, which will hereinafter be described in detail.

The transistor Q1 is adapted to perform a switching operation in response to the switching signal converted by the resistor R1 and capacitor C1, so as to generate a low signal having a falling edge depth differently determined depending on a switching period of time. The conventional glass touch sensing circuit further comprises a resistor R3 and capacitor C2 interacting to remove a noise component from the low signal from the transistor Q1 and apply the resultant low signal as a clock signal to a D flip-flop 10, which will hereinafter be described in detail.

A resistor R2 has its one end connected to an input terminal 1 which inputs a supply voltage of 5V and its other end connected in common to one end of the resistor R3, the other end of which is connected to the capacitor C2, and a

collector terminal of the transistor Q1. The low signal from the transistor Q1 is noise-removed by the resistor R3 and capacitor C2 and then applied as a clock signal to a clock terminal \overline{CK} of the D flip-flop 10.

5 The D flip-flop 10 is adapted to provide its output signal in response to the 5V supply voltage inputted by the input terminal 1, and the microprocessor 15 is adapted to provide its output signal for the control of the operation of the D flip-flop 10. The microprocessor 15 is further adapted
10 to monitor the output signal from the D flip-flop 10 and recognize the user's contact with the touch sensor 5 as a result of the monitoring.

 The D flip-flop 10 has an input terminal D, preset terminal \overline{PR} and voltage terminal VCC connected in common to
15 the input terminal 1 which inputs the 5V supply voltage. The D flip-flop 10 further has an output terminal Q connected to an input terminal IN1 of the microprocessor 15. A second output terminal OUT2 of the microprocessor 15 is connected to a clear terminal \overline{CLR} of the D flip-flop 10, which also has a
20 ground terminal GND connected to a ground voltage source. The first output terminal OUT1 of the microprocessor 15 is connected commonly to the emitter terminal of the transistor Q1, resistor R4 and capacitor C1, as stated above.

 Next, a description will be given of the operation of the
25 conventional glass touch sensing circuit with the above-

mentioned construction.

Fig. 2 is a timing diagram of output signals from the respective components in the conventional glass touch sensing circuit of Fig. 1.

5 The 5V supply voltage inputted by the input terminal 1 is always applied to the input terminal D of the D flip-flop 10. Under this condition, the microprocessor 15 provides an output signal OUT1 as shown in Fig. 2 at its first output terminal OUT1 at intervals of predetermined time to sense the user's
10 touch with the touch sensor 5.

 The output signal OUT1 from the microprocessor 15 is applied to the emitter terminal of the transistor Q1, thereby causing a potential difference to be generated between the emitter terminal and collector terminal of the transistor Q1
15 when the output signal OUT1 is low in level.

 If the user touches the touch sensor 5 under the above condition, then a voltage being charged and discharged by the resistor R1 and capacitor C1 becomes higher in level due to an electrostatic capacity of the human body, thereby causing a
20 turning-on period of time of the transistor Q1 to become longer than that when the user does not come into contact with the touch sensor 5.

 In other words, because the transistor Q1 is switched in response to the voltage being charged and discharged by the
25 resistor R1 and capacitor C1, it remains ON for a

predetermined period of time even though the user does not come into contact with the touch sensor 5. However, in the case where the user touches the touch sensor 5, the amount of charge stored in the user is added to the voltage being charged and discharged by the resistor R1 and capacitor C1, resulting in an increase in the level of the charged and discharged voltage.

As a result, the voltage being charged and discharged by the resistor R1 and capacitor C1 when the user comes into contact with the touch sensor 5 is different in level from that when the user does not do so, thereby causing turning-on periods of the transistor Q1 in both cases to be different from each other.

Upon being turned on, the transistor Q1 generates a clock signal \overline{CK} , which is low in level as shown in Fig. 2, owing to a potential difference generated between its collector terminal and emitter terminal. At this time, the low signal generated by the transistor Q1 has a falling edge depth determined depending on a turning-on period of the transistor Q1. In other words, the low signal generated by the transistor Q1 while the user comes into contact with the touch sensor 5 has a falling edge depth greater than that while the user does not do so.

The low signal generated by the transistor Q1 is noise-removed by the resistor R3 and capacitor C2 and then applied

to the clock terminal \overline{CK} of the D flip-flop 10.

The D flip-flop 10 outputs a high signal at its output terminal Q synchronously with the low signal, or low voltage, received at its clock terminal. It should be noted that, in a normal operation state, the D flip-flop 10 is enabled in response to a low signal applied under the condition of the user's contact with the touch sensor 5 and not enabled in response to a low signal applied under the condition of the user's noncontact with the touch sensor 5.

Then, the output signal from the D flip-flop 10 is applied to the input terminal IN1 of the microprocessor 15. Such a signal from the D flip-flop 10 is indicated by "INPUT SIGNAL IN1" in Fig. 2. Upon receiving the output signal from the D flip-flop 10, the microprocessor 15 recognizes the user's contact with the touch sensor 5 and then provides an output signal OUT2 at its second output terminal OUT2, which is low in level as shown in Fig. 2. The low signal from the microprocessor 15 is applied to the clear terminal \overline{CLR} of the D flip-flop 10 to clear the D flip-flop 10.

In other words, in the conventional glass touch sensing circuit, the D flip-flop 10 is enabled in response to a low signal applied to its clock terminal \overline{CK} when the user touches the touch sensor 5. Upon being enabled, the D flip-flop 10 outputs a high signal to the input terminal IN1 of the microprocessor 15, thereby causing the microprocessor 15 to

recognize the user's contact with the touch sensor 5 and output a clear signal to the D flip-flop 10 so as to initialize it.

However, the above-described conventional glass touch sensing circuit has the following disadvantages.

The clock signal to the D flip-flop has no reference value which is accurately defined to enable the operation of the D flip-flop. This clock signal is defined only by the output signal from the transistor Q1 which has a falling edge depth varying according to whether the user touches the touch sensor 5. As a result, there is a concern that the D flip-flop could be different in enabling point according to individual specifications or manufacturers thereof, thereby causing each key in the touch sensor to be different in sensitivity and performance.

The conventional glass touch sensing circuit has a further disadvantage in that it varies in sensitivity with temperature. Namely, the transistor Q1, which is switched in response to the sense signal from the touch sensor 5, is so sensitive to variations in temperature that it remains ON for a lengthier period of time at a higher temperature and for a briefer period of time at a lower temperature, respectively. The output signal from the transistor Q1 varies with such a temperature variation, resulting in a variation in the low signal being applied to the clock terminal \overline{CK} of the D flip-

flop 10. Consequently, the touch sensor becomes different in sensitivity according to variations in temperature.

SUMMARY OF THE INVENTION

5

Therefore, the present invention has been made in view of the above problems, and it is an object of the present invention to provide a glass touch sensing circuit which is capable of providing a constant performance irrespective of
10 variations in temperature.

It is another object of the present invention to provide a glass touch sensing circuit in which a touch sensor with a plurality of keys is constant in sensitivity.

In accordance with the present invention, the above and
15 other objects can be accomplished by a provision of a glass touch sensing circuit comprising a touch sensor for providing its output signal in response to a user's touch; switching means having a switching period of time differently determined depending on the level of the output signal from the touch
20 sensor; comparison means for compensating the level of a reference signal for a variation in temperature, comparing the level of an output signal from the switching means with that of the compensated reference signal and outputting a wave-shaped signal in accordance with the compared result; and
25 touch detection means responsive to an output signal from the

comparison means for detecting whether the user touches the touch sensor.

Preferably, the glass touch sensing circuit may further comprise charging/discharging means for charging and discharging a voltage which is different in level according to whether the user touches the touch sensor, and the switching time period of the switching means may be determined depending on the level of the voltage being charged and discharged by the charging and discharging means.

Preferably, the touch detection means may include signal output means for providing its output signal synchronously with the output signal from the comparison means; and recognition means for recognizing a touched key in response to the output signal from the signal output means.

More preferably, the recognition means may be adapted to recognize the input of the touched key and initialize the signal output means.

Preferably, the comparison means may include a thermistor for compensating the level of the reference signal for the temperature variation.

More preferably, the comparison means may further include a comparator having a first input terminal connected to an output terminal of the switching means, and a second input terminal for inputting a voltage determined in level by the thermistor and fixed resistors.

Preferably, the touch detection means may include a D flip-flop having a clock terminal connected to an output terminal of the comparison means, the D flip-flop being enabled in response to a clock signal being applied to the clock terminal; and a microprocessor having an input terminal connected to an output terminal of the D flip-flop, the microprocessor recognizing the user's touch with the touch sensor in response to an output signal from the D flip-flop and initializing the D flip-flop.

Preferably, the switching means may include a transistor turned on in response to the output signal from the touch sensor.

BRIEF DESCRIPTION OF THE DRAWINGS

The above and other objects, features and advantages of the present invention will be more clearly understood from the following detailed description taken in conjunction with the accompanying drawings, in which:

Fig. 1 is a circuit diagram showing the construction of a conventional glass touch sensing circuit;

Fig. 2 is a timing diagram of output signals from respective components in the conventional glass touch sensing circuit of Fig. 1;

Fig. 3 is a circuit diagram showing the construction of a

glass touch sensing circuit in accordance with the present invention;

Fig. 4 is a timing diagram of output signals from respective components in the glass touch sensing circuit of Fig. 3; and

Fig. 5 is a waveform diagram illustrating a variation in a reference voltage with temperature in accordance with the present invention.

10 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig. 3 is a circuit diagram showing the construction of a glass touch sensing circuit in accordance with the present invention.

15 As shown in Fig. 3, the glass touch sensing circuit of the present invention comprises a touch sensor 105 for outputting a sense signal in response to a user's touch, a resistor R101 and capacitor C101 interacting to convert the sense signal from the touch sensor 105 into a switching
20 signal, and a transistor Q101 having a switching period of time differently determined depending on the level of the switching signal converted by the resistor R101 and capacitor C101 (i.e., the level of a voltage being charged and discharged by the resistor R101 and capacitor C101).

25 The transistor Q101 operates as a switching device in

response to the sense signal from the touch sensor 105. It should be noted that a switching device employed in the present glass touch sensing circuit is not limited to the above transistor Q101. Any other switching device can replace
5 the transistor Q101 so long as it is capable of performing a switching operation in response to the sense signal from the touch sensor 105.

Describing the above-mentioned construction in more detail, the resistor R101 has its one end connected to an
10 output terminal of the touch sensor 105 and its other end connected to a connection point A'. Commonly connected to the connection point A' are one end of the capacitor C101, one end of a resistor R104 and a base terminal of the transistor Q101. The other end of the capacitor C101, the other end of the
15 resistor R104 and an emitter terminal of the transistor Q101 are connected in common to a first output terminal OUT11 of a microprocessor 115, which will hereinafter be described in detail. The transistor Q101 further has a collector terminal connected to an input terminal 101, which inputs a supply
20 voltage of 5V, via a resistor R102.

The present glass touch sensing circuit further comprises a comparator 120 for inputting a low signal having a falling edge depth differently determined depending on a switching period of time of the transistor Q101, comparing the level of
25 the inputted low signal with that of a reference signal

compensated for a variation in temperature and outputting a wave-shaped signal as a result of the comparison.

In other words, the comparator 120 functions to convert an output signal from the transistor Q101 into a wave-shaped
5 signal. It should be noted that a wave-shaped signal output device employed in the present glass touch sensing circuit is not limited to the above comparator 120. Any other electrical device can replace the comparator 120 so long as it is capable of performing the same function.

10 Describing the above-mentioned construction in more detail, the comparator 120 has a first input terminal connected to the collector terminal of the transistor Q101 and a second input terminal for inputting the reference signal compensated for a variation in temperature.

15 The reference signal to the comparator 120 is determined in level by reference signal determination means composed of a pair of fixed resistors R103 and R104 connected in series between the 5V supply voltage input terminal 101 and a ground voltage source, and a thermistor Rth connected in parallel to
20 the fixed resistors R103 and R104. This reference signal determination means is adapted to adjust the level of the reference signal variably with temperature.

The present glass touch sensing circuit further comprises a D flip-flop 110 for receiving an output signal from the
25 comparator 120 as a clock signal. The microprocessor 115 is

adapted to provide its output signal for the control of the operation of the D flip-flop 110, monitor an output signal from the D flip-flop 110 and recognize the user's contact with the touch sensor 105 as a result of the monitoring.

5 In other words, the D flip-flop 110 acts as a signal output device for providing its output signal based on the user's contact with the touch sensor 105. The microprocessor 115 acts as a recognition device for recognizing the user's contact with the touch sensor 105 from the output signal from
10 the D flip-flop 110. It should be noted that a signal output device and recognition device employed in the present glass touch sensing circuit are not limited to the above D flip-flop 110 and microprocessor 115. Any other devices can replace the D flip-flop 110 and microprocessor 115 so long as they are
15 capable of performing the same functions.

Describing the above-mentioned construction in more detail, the D flip-flop 110 has an input terminal D, preset terminal \overline{PR} and voltage terminal VCC connected in common to the input terminal 101 which inputs the 5V supply voltage.
20 The D flip-flop 110 further has an output terminal Q connected to an input terminal IN11 of the microprocessor 115. A second output terminal OUT12 of the microprocessor 115 is connected to a clear terminal \overline{CLR} of the D flip-flop 110, which also has a ground terminal GND connected to the ground voltage
25 source. The first output terminal OUT11 of the microprocessor

115 is connected commonly to the emitter terminal of the transistor Q101, resistor R104 and capacitor C101, as described above.

Next, a detailed description will be given of the operation of the glass touch sensing circuit with the above-described construction in accordance with the present invention with reference to Figs. 4 and 5.

Fig. 4 is a timing diagram of output signals from the respective components in the glass touch sensing circuit of Fig. 3 and Fig. 5 is a waveform diagram illustrating variations in the reference signal or voltage and transistor output signal with temperature.

The 5V supply voltage inputted by the input terminal 101 is always applied to the input terminal D of the D flip-flop 110. Under this condition, the microprocessor 115 provides an output signal OUT11 as shown in Fig. 4 at its first output terminal OUT11 at intervals of predetermined time to sense the user's touch with the touch sensor 105.

The output signal OUT11 from the microprocessor 115 is applied to the emitter terminal of the transistor Q101, the collector terminal of which is also applied with a high level voltage of about 5V via the resistor R102. As a result, a potential difference is generated between the emitter terminal and collector terminal of the transistor Q101 when the output signal OUT11 from the microprocessor 115 is low in level.

If the user touches the touch sensor 105 under the above condition, then a voltage being charged and discharged by the resistor R101 and capacitor C101 becomes higher in level due to an electrostatic capacity of the human body, thereby
5 causing a turning-on period of time of the transistor Q101 to become longer than that when the user does not come into contact with the touch sensor 105.

That is, since the transistor Q101 is switched in response to the voltage being charged and discharged by the
10 resistor R101 and capacitor C101, it remains ON for a predetermined period of time even though the user does not come into contact with the touch sensor 105. Alternatively, in the case where the user touches the touch sensor 105, the amount of charge stored in the user is added to the voltage
15 being charged and discharged by the resistor R101 and capacitor C101, resulting in an increase in the level of the charged and discharged voltage.

Consequently, the voltage being charged and discharged by the resistor R101 and capacitor C101 when the user comes into
20 contact with the touch sensor 105 is different in level from that when the user does not do so. As a result, turning-on periods of the transistor Q101 in the above two cases become different from each other.

While being turned on, the transistor Q101 generates a
25 low signal as indicated by "INPUT SIGNAL OP1" in Fig. 4,

because of a potential difference generated between its collector terminal and emitter terminal. The low signal INPUT SIGNAL OP1 generated by the transistor Q101 while the user comes into contact with the touch sensor 105 is determined to have a falling edge depth greater than that while the user does not do so.

In other words, when the user touches the touch sensor 105, the turning-on period of time of the transistor Q101 becomes longer by a predetermined value than that when the user does not come into contact with the touch sensor 105, so as to output a low signal having a greater falling edge depth.

Noticeably, the transistor Q101 is a type of electrical device sensitive to a variation in temperature. In this regard, the transistor Q101 has a longer switching period of time at a higher temperature and a shorter switching period of time at a lower temperature, respectively. Namely, as shown in Fig. 5, the switching time period of the transistor Q101 while the user touches the touch sensor 105 is different from that while the user does not do so, due to a variation in temperature, thereby causing the output signal of the transistor Q101 to vary in level.

The output signal ("INPUT SIGNAL OP1" in Fig. 4) from the transistor Q101, which varies in level, or falling edge depth, with temperature, is applied to the first input terminal of the comparator 120, the second input terminal of which inputs

the reference signal. The comparator 120 compares the two input signals with each other in level and outputs a wave-shaped signal corresponding to a level difference therebetween.

5 The reference signal to the second input terminal of the comparator 120 has a level determined to compensate for a temperature variation. To this end, commonly connected to the second input terminal of the comparator 120 are the two fixed resistors R103 and R104 connected in series between the 5V
10 supply voltage input terminal 101 and the ground voltage source and the thermistor Rth connected in parallel to the fixed resistors R103 and R104. The thermistor Rth has a resistance varying with temperature, which leads to a temperature-based variation in resistance determined by the
15 resistance of the thermistor Rth and resistances of the two fixed resistors R103 and R104. As a result, the reference signal or voltage to the second input terminal of the comparator 120 varies in level with the resistance varying in the above manner, as shown in Fig. 5.

20 In other words, the comparator 120 compares the level of the output signal from the transistor Q101 with that of the reference voltage varying as stated above and outputs a wave-shaped signal, or a clock signal \overline{CK} as shown in Fig. 4, corresponding to a level difference therebetween.

25 The wave-shaped low signal from the comparator 120 is

applied to the clock terminal \overline{CK} of the D flip-flop 110. The D flip-flop 110 outputs a high signal at its output terminal Q synchronously with the low signal, or low voltage, received at its clock terminal. As seen from time T2 in Fig. 4, the D flip-flop 110 is enabled in response to a low signal applied under the condition of the user's contact with the touch sensor 105 so as to output a high signal (indicated by "INPUT SIGNAL IN11" in Fig. 4). However, as seen from time T1 in Fig. 4, the D flip-flop 110 is not enabled in response to a low signal applied under the condition of the user's noncontact with the touch sensor 105, so as to provide no output signal.

The output signal from the D flip-flop 110, as indicated by "INPUT SIGNAL IN11" in Fig. 4, is applied to the input terminal IN11 of the microprocessor 115. Upon receiving the output signal from the D flip-flop 110, the microprocessor 115 recognizes the user's contact with the touch sensor 105 and then provides an output signal OUT12 at its second output terminal OUT12, which is low in level as shown in Fig. 4. The low signal from the microprocessor 115 is applied to the clear terminal \overline{CLR} of the D flip-flop 110 to clear the D flip-flop 110.

To sum up, the present glass touch sensing circuit is characterized in that it comprises means for comparing the level of the output signal from the switching device, or

transistor Q101, which indicates whether the user touches the touch sensor 105, with that of the reference signal and converting the output signal from the switching device into a wave-shaped signal in accordance with the compared result.

5 The present glass touch sensing circuit is further characterized in that it comprises means for determining the level of the reference signal for compensation for a variation in the output signal from the switching device with temperature.

10 Therefore, the present glass touch sensing circuit is capable of converting the output signal from the switching device into a wave-shaped signal using the reference signal compensated for a temperature variation, thereby making it possible to define the low signal to the clock terminal \overline{CK} of the D flip-flop 110 as an accurate value. As a result, the
15 microprocessor 115 in the present glass touch sensing circuit can provide a constant performance irrespective of variations in temperature to recognize the user's contact with the touch sensor 105.

20 In the present embodiment, the circuitry for recognition of the user's contact with the touch sensor 105 is implemented in the form of a unitary module. As an alternative, a plurality of modules may be implemented to recognize respective key inputs to a touch sensor, which has a plurality
25 of keys on a display screen, without departing from the scope

and spirit of the invention.

As stated above, the basic technical concept of the present invention is to compensate the level of a reference signal for a variation in temperature and output a wave-shaped
5 signal based on a touching operation using the compensated reference signal.

As apparent from the above description, the present invention provides the following advantages.

A glass touch sensing circuit can be employed in a human
10 body touch-based key input sensing system to provide a constant performance irrespective of variations in temperature so as to sense a key input based on a touching operation. Therefore, the glass touch sensing circuit has the effect of increasing the reliability of an associated product.

15 Further, the glass touch sensing circuit can accurately detect a key input based on a touching operation by sensing it using a wave-shaped signal, thereby further increasing the reliability and performance of an associated product.

Moreover, the glass touch sensing circuit can provide the
20 same performance for all key inputs in a system having a plurality of keys.

Although the preferred embodiments of the present invention have been disclosed for illustrative purposes, those skilled in the art will appreciate that various modifications,
25 additions and substitutions are possible, without departing

from the scope and spirit of the invention as disclosed in the accompanying claims.

WHAT IS CLAIMED IS:

1. A glass touch sensing circuit comprising:

a touch sensor for providing its output signal in
5 response to a user's touch;

switching means having a switching period of time
differently determined depending on the level of the output
signal from said touch sensor;

comparison means for compensating the level of a
10 reference signal for a variation in temperature, comparing the
level of an output signal from said switching means with that
of the compensated reference signal and outputting a wave-
shaped signal in accordance with the compared result; and

touch detection means responsive to an output signal from
15 said comparison means for detecting whether the user touches
said touch sensor.

2. A glass touch sensing circuit as set forth in Claim 1,
further comprising charging/discharging means for charging and
20 discharging a voltage which is different in level according to
whether the user touches said touch sensor, said switching
time period of said switching means being determined depending
on the level of said voltage being charged and discharged by
said charging and discharging means.

25

3. A glass touch sensing circuit as set forth in Claim 1,
wherein said touch detection means includes:

signal output means for providing its output signal
synchronously with said output signal from said comparison
5 means; and

recognition means for recognizing a touched key in
response to the output signal from said signal output means.

4. A glass touch sensing circuit as set forth in Claim 3,
10 wherein said recognition means is adapted to recognize the
input of said touched key and initialize said signal output
means.

5. A glass touch sensing circuit as set forth in Claim 1,
15 wherein said touch detection means includes:

a D flip-flop having a clock terminal connected to an
output terminal of said comparison means, said D flip-flop
being enabled in response to a clock signal being applied to
said clock terminal; and

20 a microprocessor having an input terminal connected to an
output terminal of said D flip-flop, said microprocessor
recognizing the user's touch with said touch sensor in
response to an output signal from said D flip-flop and
initializing said D flip-flop.

25

6. A glass touch sensing circuit as set forth in Claim 1, wherein said comparison means includes a thermistor for compensating the level of said reference signal for the temperature variation.

5

7. A glass touch sensing circuit as set forth in Claim 6, wherein said comparison means further includes a comparator having a first input terminal connected to an output terminal of said switching means, and a second input terminal for
10 inputting a voltage determined in level by said thermistor and fixed resistors.

8. A glass touch sensing circuit as set forth in Claim 1, wherein said switching means includes a transistor turned on
15 in response to said output signal from said touch sensor.

ABSTRACT OF THE DISCLOSURE

A glass touch sensing circuit which is capable of accurately detecting a sense signal resulting from a user's touch under no influence of variations in temperature. The glass touch sensing circuit is adapted to compare the level of an output signal from a switching device, which indicates whether the user touches a touch sensor, with that of a reference signal and convert the output signal from the switching device into a wave-shaped signal in accordance with the compared result. The present glass touch sensing circuit is further adapted to determine the level of the reference signal for compensation for a variation in the output signal from the switching device with temperature. Therefore, the glass touch sensing circuit can be employed in a human body touch-based key input sensing system to provide a constant performance irrespective of a variation in temperature so as to sense a key input based on a touching operation.

Fig 1

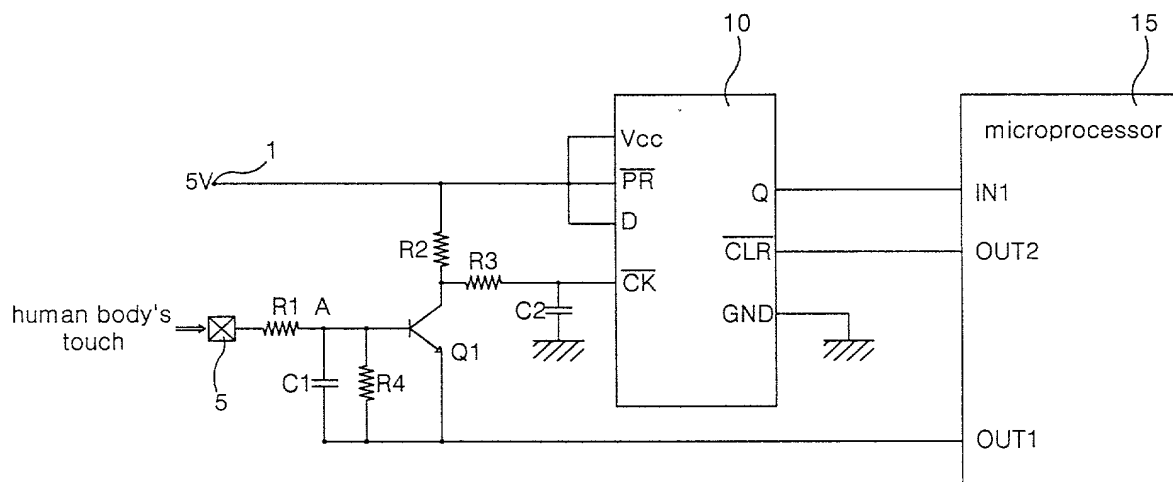


Fig 2

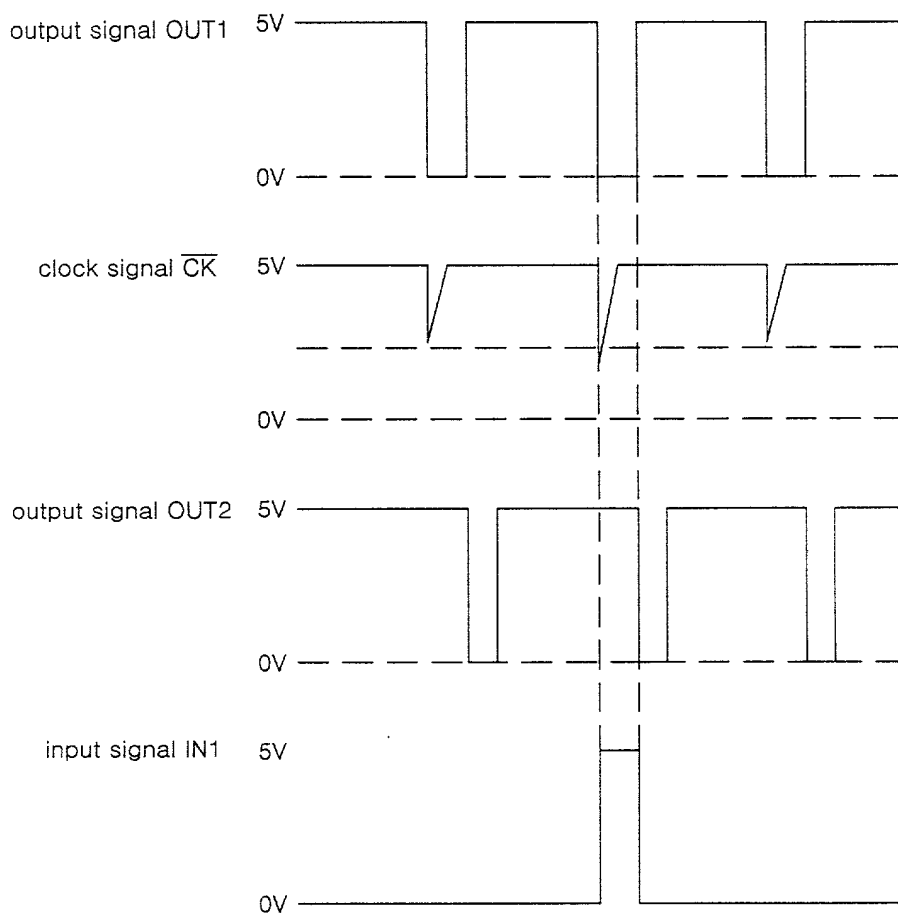


Fig 3

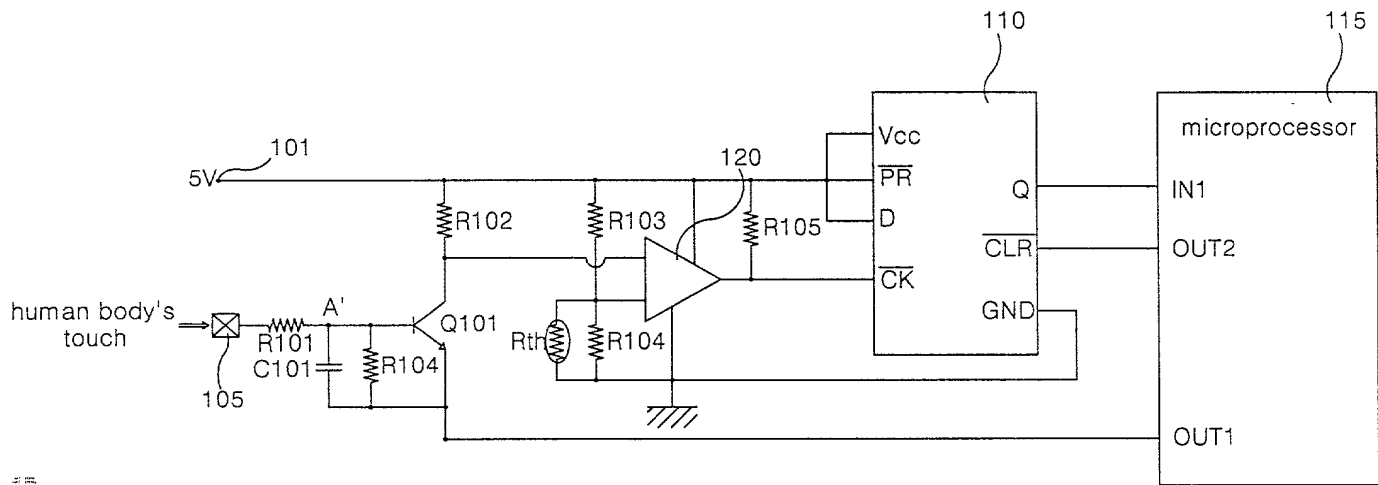


Fig 4

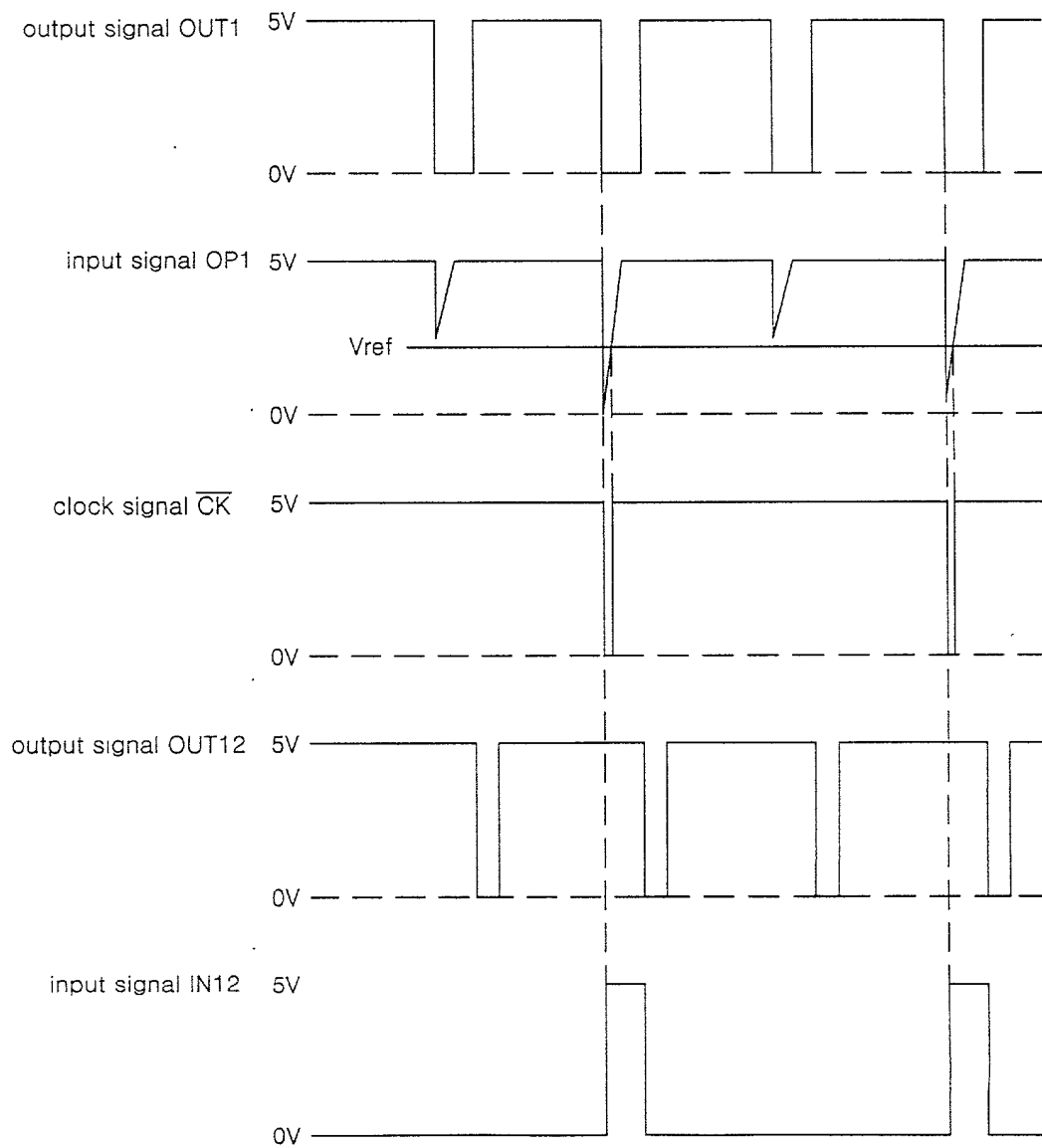
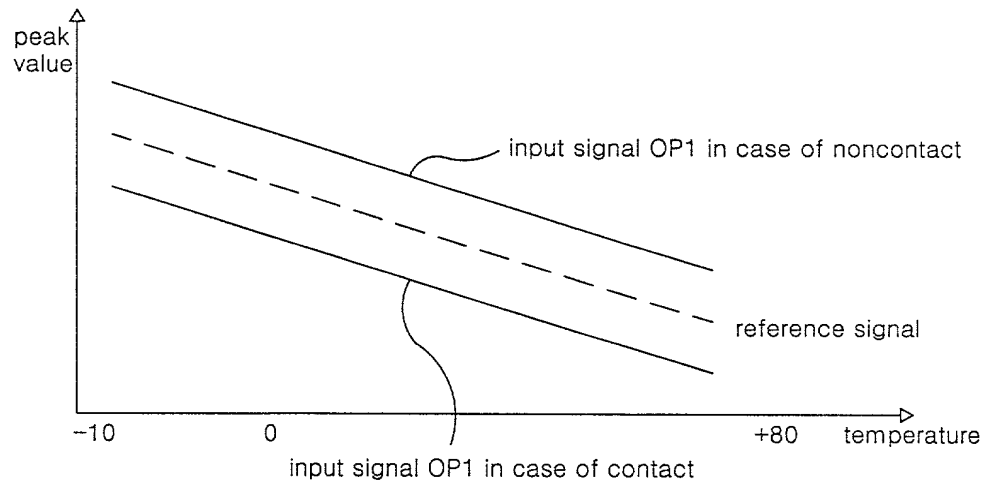


Fig 5



Docket No.: _____

DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter claimed and for which a patent is sought on the invention entitled GLASS TOUCH SENSING CIRCUIT, the specification of which

☒ [X] is attached hereto ☐ [] was filed on _____ as Application Serial No. _____ and was _____ amended on _____ (if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is known to me to be material to patentability in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or 365 (b) of any foreign application(s) for patent or inventor's certificate, or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s):

Number

Country

Foreign Filing Date

Month/Day/Year

2000-32413

Korea

June 13, 2000

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional application(s) listed below.

Application Number(s):

Filing Date (Month/Day/Year)

I hereby claim the benefit under 35 U.S.C. 120 of any United States application(s), or 365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT international application in the manner provided by the first paragraph of 35 U.S.C. 112, I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

**Prior U. S. Application
or PCT Parent Number**

Filing Date (Month/Day/Year)

Parent Patent Number (if applicable)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

I hereby appoint the following attorney(s) and/or agent(s): Daniel Y.J. Kim, Registration No. 36,186 and Mark L. Fleshner, Registration No. 34,596; Carl R. Wesolowski, Registration No. 40,372, John C. Eisenhart, Registration No. 38,128, Rene A. Vazquez, Registration No. 38,647; and Michael J. Cornelison, Registration No. 40,395; and Carol L. Druzbeck, Registration No. 40,287, all of

FLESHNER & KIM
P. O. Box 221200
Chantilly, Virginia 20153-1200

with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and all future correspondence should be addressed to them.

Full name of sole or first inventor: Sang-Seog Kang

Inventor's signature:



Date: 10, 25, 2000

Residence: Changwon Gyeongnam, Korea

Citizenship: Republic of Korea

Post Office Address: H-206, LG Electronics dormitory 14-5 Gaeumjung-Dong, Changwon
Kyeongnam, Korea

Full name of joint inventor(s):

Inventor's signature:

Date:

Residence:

Citizenship:

Post Office Address:

Full name of joint inventor(s):

Inventor's signature:

Date:

Residence:

Citizenship:

Post Office Address: